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			ATTORNEY DOCKET NO.
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	T19-25912
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TEXAS INSTRUMENTS INCORPORATED
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EXAMINERFOSTER, D

ART UNIT 2841

DATE MAILED: 10/23/01

PAPER NUMBER

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks



Notice of Allowability

Application No.

Applicant(s)

09/115,444

Chen et al.

Examiner

David Foster

Art Unit 2841

The MAILING DATE of this communication appears on the cov	er sneet with the correspondence address			
All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS (or previously mailed), a Notice of Allowance and Issue Fee Due or other app THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. The initiative of the Office or upon petition by the applicant. See 37 CFR 1.3	ropriate communication will be mailed in due course. his application is subject to withdrawal from issue at			
This communication is responsive to 8/24/2001				
2. X The allowed claim(s) is/are 1-22				
3. X The drawings filed on Jan 19, 2001 are acceptable as for	ormal drawings.			
Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).				
a) \square All b) \square Some* c) \square None of the:				
1. Certified copies of the priority documents have been receive	red.			
2. Certified copies of the priority documents have been received	red in Application No			
3. Copies of the certified copies of the priority documents have application from the International Bureau (PCT Rule 17.)	2(a)).			
*Certified copies not received:				
5. Acknowledgement is made of a claim for domestic priority under				
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communoted below. Failure to timely comply will result in ABANDONMENT of this EXTENDABLE FOR SUBMITTING NEW FORMAL DRAWINGS, OR A SUBSTIT for complying with the REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL I	application. THIS THREE-MONTH PERIOD IS NOT			
6. Note the attached EXAMINER'S AMENDMENT or NOTICE OF IN reason(s) why the oath or declaration is deficient. A SUBSTITUTE	FORMAL APPLICATION (PTO-152) which gives JTE OATH OR DECLARATION IS REQUIRED.			
7. Applicant MUST submit NEW FORMAL DRAWINGS				
(a) \square including changes required by the Notice of Draftsperson's Pa	etent Drawing Review (PTO-948) attached			
1) 🗌 hereto or 2) 🗀 to Paper No				
(b) \square including changes required by the proposed drawing correction approved by the examiner.	on filed, which has been			
(c) including changes required by the attached Examiner's Amendment/Comment or in the Office action of Paper No				
Identifying indicia such as the application number (see 37 CFR 1.84 drawings should be filed as a separate paper with a transmittal letter	(c)) should be written on the drawings. The r addressed to the Official Draftsperson.			
8. Note the attached Examiner's comment regarding REQUIREMEN	T FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Any reply to this letter should include, in the upper right hand corner, to NUMBER). If applicant has received a Notice of Allowance and Issue F the NOTICE OF ALLOWANCE should also be included.	he APPLICATION NUMBER (SERIES CODE/SERIAL ee Due, the ISSUE BATCH NUMBER and DATE of			
Attachment(s)	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □			
1 Notice of References Cited (PTO-892)	2 Notice of Informal Patent Application (PTO-152)			
3 Notice of Draftsperson's Patent Drawing Review (PTO-948)	 4 Interview Summary (PTO-413), Paper No 6 Examiner's Amendment/Comment 			
5 Information Disclosure Statement(s) (PTO-1449), Paper No(s).	6			
7 Examiner's Comment Regarding Requirement for Deposit of Biological Material	O (A) EXAMINION & STATEMENT OF RESSOURT OF MICHAELICE			
9 Other				

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DETAILED ACTION

HIGH DENSITY INTERNAL BALL GRID ARRAY INTEGRATED CIRCUIT

PACKAGE

Chen et al.

Allowable Subject Matter

- 1. Claims 1-22 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: Cited prior art does not teach nor suggest a combination of an interconnect structure for a semiconductor chip having a substrate with a plurality of pads, routing strips, wire bonding, a chip wherein the electrical conductors are physically attached to the chip and bonding pads such that each bonding pad is aligned with openings in the substrate between bonding pads and routing strips.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The best art to consider with this application can be found in: Yamaguchi

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(5,886,876), Marcantonio (5,796,170), Takamichi et al. (6,054,755), Ha et al. (5,767,446), Chia et al. (5,973,393), Selna (5,640,048), Gaudenzi et al. (5,313,366), Hoppe (4,843,225). Yamaguchi discloses a surface mounted semiconductor package having a substrate with a stacked structure wherein the semiconductor device mounted on the top of the substrate and provided with electrode pads, Marcantonio discloses BGA integrated circuit having a vent hole, substrate, pads and ball grid array, Takamichi et al. disclose a semiconductor package with moisture vapor relief wherein moisture vapor in the package is released outside through the vent hole, Ha et al. disclose a semiconductor package having a substrate, chip, ball grid and a molded seal, Chia et al. disclose an apparatus and method for packaging and IC, Selna discloses a threelayer BGA package having vias, Gaudenzi et al. disclose a surface mount carrier, pads, a chip and solder balls and Hoppe discloses an identification card with and IC wherein polymer is injected in the cavity. However, none of the above cited prior have the overall combination of an interconnect structure for a semiconductor chip having a substrate with a plurality of pads, routing strips, wire bonding, a chip wherein the electrical conductors are physically attached to the chip and bonding pads such that each bonding pad is aligned with openings in the substrate between bonding pads and routing strips as does applicant.

4. Any inquiry concerning to this communication or earlier communications from the Examiner should be directed to David Foster whose telephone number is (703) 308-1763. The examiner can normally be reached on Monday through Thursday and alternate Fridays.

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If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's Supervisor, Leo P. Picard, who can be reached on (703) 308-0538. The fax phone number for the organization where this application or proceeding is assigned is (703)308-7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

L. P. Puril

DAF

October 16, 2001

Leo P. Picard Supervisory Patent Examiner Technology Center 2800 Page 4